Abstracts

New boundary and edge conditions for real planar circuits

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Practical planar circuits involve metallic edges with finite conductivity and non-ideal dielectrics: it is usually more or less implicitly assumed that fields and induced currents behave as if conductors and dielectrics were ideal. In this paper we show such an assumption to be erroneous and that the presence of real conductors and dielectrics leads to a simpler and more physical picture.

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